

Roll No.

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BT-4 / M-19

**FUNDAMENTALS OF MICROPROCESSOR
INTERFACING AND APPLICATION**

Paper-IT-210 N

Time allowed : 3 hours]

[Maximum marks : 75

Note : Attempt any five questions in all, selecting at least one from each unit.

Unit-I

1. With a neat and clean sketch explain the internal architecture of 8085 Microprocessor. 15
2. Draw and explain the timing diagram for opcode fetch and memory write cycle with example. 15

Unit-II

3. Describe 8085 addressing modes in detail with suitable examples. 15
4. List out five categories of 8085 instruction and give four examples of each group. 15

Unit-III

5. (a) With suitable examples how I/O devices are connected using memory mapped I/O and peripheral I/O. 8
- (b) Design a microprocessor system to interface 8K × 8 RAM. 7

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6. Explain the interrupt structure supported by 8085 in detail. 15

Unit-IV

7. Discuss the function of DMA controller (8237) with suitable diagram. 15
8. Write an assembly language programme to interface matrix keyboard with 8085 microprocessor. 15

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